Abstract

Thyristor arrangement with recovery time protection

Thyristor arrangement having a main thyristor (1), at least one auxiliary thyristor (2), a resistance device (3), which electrically connects the auxiliary thyristor and the main thyristor to one another, and an optical triggering device (4) for breakover triggering of the main thyristor via the auxiliary thyristor and the resistance device, the resistance device defining a time-dependent ohmic resistance in such a way that the value thereof is relatively large during a switch-on phase of the main thyristor and relatively small during a current-carrying phase of the main thyristor.

Figure 1